

EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts  
 BRS: 1 and (1  
 Pending  
 Active  
 L1: (678) (327/93 or 327/94 or 327/95 or  
 L2: (719) (327/93 or 327/94 or 327/95 or  
 L3: (53) 2 and (lcd or led or (Liquid adj  
 L4: (1005) (349/149 or 349/150 or 349/151 or 349/152)  
 L5: (93) 4 and (csacade or delay)  
 Failed  
 Saved  
 Favorites  
 Tagged (2)  
 UDC  
 Queue  
 Trash

said connection pads being unconnected.

3. The display of claim 1, wherein said plural gate drivers on said second substrate and said plural gate drivers on said second substrate have plural source drivers thereon for driving signal lines that are associated with respective ones of said plural source drivers and to which one said wiring second substrates is connected.

4. The display of claim 1, further comprising a third edge of said panel between said first and second substrates having plural source drivers thereon for driving signal lines that are associated with respective ones of said plural source drivers and to which one said wiring second substrates is connected.

BRS form IS&R form Image Text HTML

Type	L #	Hits	Search Text	DBs	
1	BRS	L1	678	(327/93 or 327/94 or 327/95 or 327/96).ccls.	USPAT
2	BRS	L2	719	(327/93 or 327/94 or 327/95 or 327/96).ccls.	USPAT; US-PGPUB
3	BRS	L3	53	2 and (lcd or led or (Liquid adj crystal))	USPAT; US-PGPUB
4	BRS	L4	1005	(349/149 or 349/150 or 349/151 or 349/152).ccls.	USPAT; US-PGPUB
5	BRS	L5	93	4 and (csacade or delay)	USPAT; US-PGPUB

Start Details HTML

09/893, 598

10/11/2004  
 Paul Bell

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File View Edit Tools Window Help

Drafts  
Pending  
Active  
L1: (261) differential adj input adj circuit  
L2: (38) 1 and (lcd or crystal)  
L3: (1107) differential adj stage  
L4: (73) 3 and (lcd or crystal)  
L5: (69) 4 not 2  
L6: (107) 2 or 5  
**L7: (12) 6 and (reduce adj power)**  
Failed  
Saved  
Favorites  
Tagged (6)  
UDC  
Queue  
Trash

B-Prof List Browse Queue Clear  
DBs USPAT Plurals  
Default operator: OR  
Highlight all hi terms initially  
6 and (reduce adj power)

BRS f... IS&R ... Image Text HTML

U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6614295 B2	20030902	84	Feedback-type amplifier circuit and driver circuit	327/563	327/541
2	<input type="checkbox"/>	<input type="checkbox"/> US 6313830 B1	20011106	13	Liquid crystal display	345/204	327/530; 345/100;
3	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6157203 A	20001205	20	Input circuit with improved operating margin using a	326/21	326/23; 326/34;
4	<input type="checkbox"/>	<input type="checkbox"/> US 6140834 A	20001031	20	Semiconductor integrated circuit	326/21	326/33; 326/81;
5	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 6069605 A	20000530	60	Liquid crystal driving device, liquid crystal	345/98	345/100
6	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5784327 A	19980721	66	Memory cell array selection circuits	365/218	257/E21.682; 257/E27.103;
7	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5777515 A	19980707	21	Operational amplifier apparatus	330/257	330/292
8	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5659514 A	19970819	65	Memory cell and current mirror circuit	365/218	257/E21.682; 257/E27.103;
9	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5535167 A	19960709	65	Non-volatile memory circuits, architecture	365/218	257/E21.682; 257/E27.103;
10	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5440518 A	19950808	67	Non-volatile memory circuits, architecture and	365/218	257/E21.682; 257/E27.103;
11	<input checked="" type="checkbox"/>	<input type="checkbox"/> US 5278785 A	19940111	69	Non-volatile memory circuits and architecture	365/185.16	257/E21.682; 257/E27.103;

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